

AMENDED FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. I-2-135.2US	SERIAL NO. 10/053,969
	APPLICANT Stephen E. Terry	
	FILING DATE January 22, 2002	GROUP 2662

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
02/14	AA	5	3	1	7	5	6	2		5/94	Nardin et al.			
1	AB	5	8	0	2	3	1	0		9/98	Rajaraman			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
														YES	NO
AC	E	P	09	12	01	5	A2			4/99	Europe				
AD	E	P	09	12	01	5	A3			5/99	Europe				
AE	E	P	09	77	39	3	A1			2/00	Europe				
AF	W	O	96	08	9	3	5			3/96	PCT				
AG	W	O	99	09	7	7	5			2/99	PCT				
AH	W	O	98	47	2	5	3			10/98	PCT				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AI	Chi-Qing Yang and Alapati V. S. Reddy, "A Taxonomy for Congestion Control Algorithms in Packet Switching Networks", IEEE Network, July/August 1995, pp. 34-45
AJ	TS 25.321 V2.0.1 MAC Protocol Specification, TSG-RAN Working Group 2 (Radio Layer 2 and Radio Layer 3), Berlin, Germany, May 25-28, 1999.
AK	Stephen E. Terry, FACH Scheduling, Prioritization and Queue Management, 3GPP TSG-RAN WG2 #4, Berlin, Germany, May 25-28, 1999.

EXAMINER

Donald Z. Hill

DATE CONSIDERED

3/1/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		ATTY. DOCKET NO. I-2-0135.2US		SERIAL NO. 10/053,969										
		APPLICANT Stephen E. Terry												
		FILING DATE January 22, 2002		GROUP 2662										
U.S. PATENT DOCUMENTS														
EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	EXAMINER APPROPRIATE	
<i>Drum</i>	AL	4	6	3	0	2	6	1	12/1986	Irvin				
	AM	5	2	7	8	8	2	8	01/1994	Chao				
	AN	5	7	2	9	5	4	0	03/1998	Wegrzyn				
	AO	5	8	3	8	6	7	7	11/1998	Kozaki et al.				
	AP	6	0	9	1	7	2	5	07/2000	Cheriton et al.				
	AQ	6	1	0	5	0	7	0	08/2000	Gaylord				
	AR	6	1	2	2	2	5	3	09/2000	Jones				
	AS	6	4	2	4	6	2	4	07/2002	Galand et al.				
	AT	6	4	9	6	5	1	3	12/2002	Takamichi				
FOREIGN PATENT DOCUMENTS														
		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
													YES	NO
<i>Drum</i>	AU	4	3	0	5	7	0	A2	06/1991	EP				
	AV	8	7	7	5	1	2	A2	11/1998	EP				
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)														
<i>Drum</i>	AW	Chao et al., "Queue Management with Multiple Delay and Loss Priorities for ATM Switches," IEEE International Conference on Communications, 1994, ICC 94, SUPERCOMM. New Orleans, LA. May 1-5, 1994, vol. 2, pgs. 1184-1189.												
	AX	Liebeherr et al., "Priority Queue Schedulers with Approximate Sorting in Output-Buffered Switches," IEEE Journal on Selecte Areas in Communications, Charlottesville, VA. June 1999, vol. 17, iss. 6 pgs. 1127-1144												
EXAMINER: <i>Donald Smith</i>										DATE CONSIDERED 3/1/04				

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